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Tunable capacitor for superconducting qubits using an InAs/InGaAs heterostructure

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Abstract

Adoption of fast, parametric coupling elements has improved the performance of superconducting qubits, enabling recent demonstrations of quantum advantage in randomized sampling problems. The development of low loss, high contrast couplers is critical for scaling up these systems. We present a blueprint for a gate-tunable coupler realized with a two-dimensional electron gas in an InAs/InGaAs heterostructure. Rigorous numerical simulations of the semiconductor and high frequency electromagnetic behavior of the coupler and microwave circuitry yield an on/off ratio of more than one order of magnitude. We give an estimate of the dielectric-limited loss from the inclusion of the coupler in a two qubit system, with coupler coherences ranging from a few to tens of microseconds.

1. Introduction

Tunable couplers for superconducting qubits, previously thought of as long-term investments in future quantum computers and building blocks towards demonstrating high fidelity two qubit gates [1, 2], are now center-pieces of large scale superconducting qubit-based quantum computers. The early quantum advantage demonstration [3] owes its success, in part, to the two-qubit gate fidelities across the chip facilitated by fast, tunable couplers. Often tunable couplers are realized as mutual inductances or effective capacitances between nearest-neighbor qubits and tuned by flux-biased superconducting quantum interference devices (SQUIDs), naturally integrating with both fixed and flux-tunable superconducting qubit fabrication capabilities [4].

Advancements in the growth of superconductor-semiconductor (super-semi) structures for use in gate-tunable Josephson junctions have led to proposals [5, 6] and experimental demonstrations of voltagecontrolled coupling schemes, superconducting quantum storage units [7], and readout resonator buses [8]. Unlike their conventional transmon qubit [9] counterparts, whose energies are either fixed by their shunt capacitors or tuned with magnetic fluxes threading SQUID loops [10], these hybrid quantum systems consist of epitaxial III–V semiconductor layers whose properties are tunable with precise composition control and applied electric fields.

Challenges in optimizing materials and fabrication processes remain to realize high coherence gatemon [11] qubits and other voltage-tunable super-semi devices. These gatemon qubits differ from their flux-tunable and fixed frequency transmon counterparts in that their Josephson junctions are formed by superconductor-semiconductor-superconductor junctions and their Josephson energies E_J are tunable by an external electric potential. Although achieving coherences of two dimensional electron gas (2DEG)-based gatemon qubits at parity with conventional transmon-like qubits remains an open area of research, similar systems acting as low participation couplers still offer fast, high contrast control with a tolerable reduction in system coherence. Recent experimental demonstrations of tunable resonators using the same materials stack, achieved an on/off coupling ratio between resonators of one order of magnitude, a promising first step towards realizing fast, voltage-tunable couplers [12].

We propose a voltage-controlled capacitive coupling element between neighboring superconducting qubits using a III–V semiconductor 2DEG in an InAs/InGaAs heterostructure. The capacitance of the coupler tunes as a function of a gate voltage or series of gate voltages applied to the 2DEG, repelling electrons away from the region underneath the gates. By 'parting the sea of electrons' in the quantum well, the coupler straddles two limits—fully conducting and fully depleted or insulating. In the intermediate region, the area of the depleted charges acts as an effective dielectric of some width *d*, and the capacitance of the coupler decreases with increasing width, as one might expect a parallel plate capacitor to behave as the separation between the plates increases. From this simple operational principle and reduction in sensitivity to bias line fluctuations, we expect such a coupler to be a drop-in replacement for SQUID-based inductive couplers [13].

Additional capacitors between the coupler and the qubits may minimize unwanted electric field coupling to other qubits. This is an improvement over SQUID-based couplers, where stray magnetic fields can lead to classical cross-talk between qubits [14]. We suspect that the 2DEG coupler may introduce more charge noise than the inductive couplers through the voltage control lines, yet transmon qubits, our initial targets for qubit-coupler integration testing, are exponentially insensitive to this charge noise.

The structure of the paper is as follows. We start by presenting a conceptual design of the coupler in section 2.1. In section 2.2, we formulate rigorous numerical models of the 2DEG coupler, starting with COMSOL semiconductor electron density calculations, followed by additional electrostatic and frequency-domain COMSOL simulations of the capacitance and admittance matrices, respectively. That section concludes with a summary of the dielectric and other loss mechanisms present in the III–V semiconductor and dielectric materials in the coupler. Section 3.2 details our ANSYS high frequency simulation software (HFSS) simulations of a prototypical two transmon qubit circuit coupled by a lumped element capacitor representing the 2DEG coupler. We apply energy participation ratio (EPR) techniques [15] to extract the Hamiltonian matrix elements in the dispersive regime, and extend these calculations to compute the charge-charge interaction matrix elements between the two transmon qubits. These analyses give similar results when considering a single lumped element variable capacitor representing the coupler compared to a full parasitic capacitance model of the coupler from our electrostatic COMSOL simulations.

2. Methods and modeling

2.1. Conceptual design

Inspired by textbook parallel plate capacitors, our coupler design relies on electronic control of the carrier concentration between two contacts to modify the effective parallel plate capacitor geometry seen by neighboring qubits. We consider a proximitized semiconductor [16] sandwiched between two transmon-like qubits with large capacitor plates patterned on top and a metal-oxide gate separating the two plates. Applying a negative gate voltage decreases the carrier concentration directly below the gate, modifying the capacitor geometry by increasing the effective separation of the parallel plates. The high electron mobility of the carriers in the 2DEG, exceeding 14 000 cm² V⁻¹ s⁻¹ at 20 mK, [17] allows for fast gating, enabling parametric interactions with rapidly oscillating gate voltages.

This concept generalizes to multiple gates, where each region of low electron concentration corresponds to an effective dielectric and each region with high electron concentration acts a conductor. The effective capacitance seen by the two qubits is the series combination of the individual capacitances defined by alternating effective dielectrics and conductors. Similar gating schemes have been proposed for nonreciprocal devices [18], tunable quantum buses [8], and controlled-Z gates [5].

Apart from the aforementioned experimental demonstrations of these devices, few modeling efforts, if any, have explored the practical considerations of realizing such couplers. The following numerical simulations aim to address those concerns by estimating the capacitive tuning range in the presence and absence of parasitic capacitances, calculating relevant interaction matrix elements, and providing an upper bound on the losses inherited by the system from the dielectric materials of the coupler.

2.2. Classical modeling

2.2.1. Semiconductor 2DEG calculations

To estimate the capacitance of the 2DEG coupler, we compute the electron concentrations in the active region of the device (InGaAs/InAs/InGaAs layers) using the COMSOL Multiphysics Semiconductor Module [19]. Equilibrium solutions to the drift-diffusion equations with Fermi–Dirac statistics serve to identify regions of high depletion under the gate(s) when applying negative voltages on the order of a few volts, overcoming the work function of the aluminum gate contact.

We use a layer structure typical of gatemon qubits as in figure 1 and refer to this structure as the 'device stack' [17, 20, 21]. To model the device stack in COMSOL, we specified the following electronic properties of



Figure 1. Schematic of the 2DEG coupler as modeled in COMSOL Multiphysics based on [13]. An aluminum contact deposited on Al_xO_y defines the gate terminal. We abbreviate the fixed composition ternary III-V alloys $In_{0.81}Ga_{0.19}As$ and $In_{0.81}Al_{0.19}As$, as InGaAs and InAlAs, respectively. Not shown or modeled is the superlattice graded buffer layer between InP and InAlAs [17, 20, 21].

Table 1. Materials parameters used in the COMSOL semiconductor module calculations. InGaAs and InAlAs abbreviate $In_{0.81}Ga_{0.19}As$ and $In_{0.81}Al_{0.19}As$. m_0 corresponds to the rest mass of an electron (0.511 MeV c⁻²). Out-of-plane effective electron and hole masses of InAs are set to m_0 in the model to simulate 2DEG confinement in the *xy*-plane. Electron mobilities for InAs, InGaAs, and InAlAs are all set to the same value as extracted from measurements of a similar device at millikelvin temperatures [17]. Values not in parenthesis (in parenthesis) correspond to electron (hole) properties.

	InAs	InGaAs	InAlAs	InP
$\overline{E_g (eV)}$	0.354	0.473	0.752	1.344
ΔE_c (eV)	_	0.200	0.201	0.12
$\varepsilon_{ m r}$	15.15	14.03	13.13	12.9
$N_c ({\rm cm}^{-3})$	$6.6 imes 10^{-18}$	$1.4 imes 10^{-17}$	$2.1 imes 10^{-17}$	
$N_{\nu} ({\rm cm}^{-3})$	$8.73 imes 10^{-16}$	$6.4 imes 10^{-18}$	$7.8 imes10^{-18}$	
χ (eV)	4.9	4.7	4.5	4.38
$\mu_{n(p)}^{lf} [cm^2 V^{-1} s^{-1}]$	$14.4 imes 10^{-3} (500)$	$14.4 \times 10^{-3} (450)$	$14.10^{-3} (384)$	$5.4 imes 10^{-3} (200)$
$m_{n(p),c}^{*}[m_0]$	0.023 (1.00)	0.03 (0.25)	0.04 (0.31)	0.08 (0.60)

the semiconductor materials and the dielectric constant of the gate oxide: electron and hole effective conduction band masses $m_{n(p),c}^*$, low-field mobilities $\mu_{n(p)}^{lf}$, band gap energies E_g , conduction band offsets ΔE_c between neighboring semiconductors, dielectric constants ε_r , and effective densities of states for the conduction and valence bands $N_{c(v)}$. Taking the electron affinity χ for InAs as given by the COMSOL material library, we calculated the remaining affinities using Anderson's affinity rule and the conduction band offsets of each material [22]. As in [21], we included a silicon delta-doping 6 nm below the interface between the InAlAs and lower InGasAs layers. COMSOL approximates such a doping profile with the Geometry Doping profile, which we select a Gaussian profile with a width of 0.1 nm. Table 1 gives a summary of the material parameters used in these semiconductor simulations; see appendix A for detailed calculations of the energy gaps, effective masses, and conduction band offsets for In_xGa_{1-x}As and In_xAl_{1-x}As as functions of the composition parameter (x).

We specify the geometry in figure 1 using the native COMSOL CAD editor to define domains (surfaces or planes) and boundaries (lines or edges), solving for the electron density in the domains and on the boundaries. Electronic properties assigned to each domain follow from table 1. We model the terminals (source – 1, drain – 2, gate – 3, as in figure 2) as Terminal boundary conditions with voltages V_1 , V_2 , V_3 and contact work functions $\Phi_{c,1}$, $\Phi_{c,2}$, $\Phi_{c,3} = 4 V$ [19].

We selected the density gradient discretization scheme [23] in COMSOL to approximate the quantum confinement effects in the 2DEG more efficiently than a self-consistent Schrödinger-Poisson equation calculation. The density gradients modify the equilibrium electron (n) and hole (p) concentrations by [19]



Figure 2. Schematic of two transmon qubits and the 2DEG coupler. Blue regions correspond to low electron concentration or effective dielectrics and red regions correspond to high electron concentration or effective conductors. We use the labeling of the voltage nodes V_i throughout the text, where nodes 1 and 2 correspond to source and drain terminals, and node 3 refers to the gate terminal.

$$n = N_{\rm c} F_{1/2} \left(\frac{E_{\rm fn} - E_{\rm c} + q V_{\rm n}^{\rm DG}}{k_{\rm B} T} \right) \tag{1}$$

$$p = N_{\rm v} F_{1/2} \left(\frac{E_{\rm v} - E_{\rm fp} + q V_{\rm p}^{\rm DG}}{k_{\rm B} T} \right) \tag{2}$$

$$N_{\rm c(v)} = \left(\frac{2m_{\rm n(p)}^*\pi k_{\rm B}T}{h^2}\right)^{3/2},\tag{3}$$

where $E_{c(v)}$ is a given material's conduction (valence) band edge, $E_{fn(p)}$ are the electron (hole) quasi-Fermi level energies, $F_{1/2}(\eta)$ is the Fermi–Dirac integral [24], k_B is Boltzmann's constant, T is the temperature of the device (approximate temperature of the mixing chamber stage of typical dilution refrigerators ~10 mK), and q is the charge of an electron or hole. The quantum potentials $V_{n(p)}^{DG}$ are defined in terms of the density gradients by [19]

$$\nabla \cdot \left(\mathbf{b}_{\mathrm{n}} \nabla \sqrt{n} \right) = \frac{1}{2} \sqrt{n} \, V_{\mathrm{n}}^{\mathrm{DG}} \tag{4}$$

$$\nabla \cdot \left(\mathbf{b}_{\mathrm{p}} \nabla \sqrt{p} \right) = \frac{1}{2} \sqrt{p} \, V_{\mathrm{p}}^{\mathrm{DG}},\tag{5}$$

with the density gradient tensors $\mathbf{b}_{n(p)}$ for electrons (holes) expressed in terms of the effective mass tensors $\mathbf{m}_{n(p)}^*$

$$\mathbf{b}_{\mathrm{n}} = \frac{\hbar^2}{12q} \left[\mathbf{m}_{\mathrm{n}}^* \right]^{-1} \tag{6}$$

$$\mathbf{b}_{\mathrm{p}} = \frac{\hbar^2}{12q} \left[\mathbf{m}_{\mathrm{p}}^* \right]^{-1}. \tag{7}$$

Note the distinction between the scalar effective masses $m_{n(p)}^*$ and, the effective mass tensors $\mathbf{m}_{n(p)}^*$. Anisotropy in the effective mass tensors emulates the quantum confinement effects in the 2DEG, constraining electron movement to one plane.

For the remaining materials, Al_2O_3 and air, we used the electric charge conservation interface, including the following constitutive relations for each dielectric in terms of its electric permittivity tensor $\boldsymbol{\varepsilon}$ [19]

$$\mathbf{D} = \varepsilon_0 \boldsymbol{\varepsilon} : \mathbf{E},\tag{8}$$

where **D** is the electric displacement field, ε_0 is the permittivity of free space, **E** is the electric field, and ε : **E** is a tensor contraction (matrix vector product) between ε and **E**. Modeling these regions as pure dielectrics reduces the size of the system of equations relative to a drift-diffusion calculation applied to the materials that behave as perfect insulators, air and oxide layers. We excluded the superlattice graded buffer between the

InAlAs and InP, as we expect the electric fields and carrier concentrations to be negligible in those regions and the additional computational cost (number of degrees of freedom solved for in the COMSOL model) would not improve the accuracy of our estimates of the capacitances and conductances of the coupler that will be largely determined by the charge dynamics in and near the active region (InGaAs/InAs/InGaAs).

2.2.2. Electric currents admittance matrix calculations

To extract the conductance matrix and verify the capacitance matrix of the device under high frequency excitation agrees with the electrostatic result, we use the harmonic perturbation option in the COMSOL Semiconductor module to compute the admittance matrix **Y** defined in terms of the *N* terminal voltages V_k and currents I_k [25]

$$\begin{pmatrix} I_1 \\ I_2 \\ \vdots \\ I_N \end{pmatrix} = \begin{pmatrix} Y_{11} & Y_{12} & \dots & Y_{1N} \\ Y_{21} & Y_{22} & \dots & Y_{2N} \\ \vdots & \vdots & & \vdots \\ Y_{N1} & Y_{N2} & \dots & Y_{NN} \end{pmatrix} \begin{pmatrix} V_1 \\ V_2 \\ \vdots \\ V_N \end{pmatrix}.$$
(9)

In the frequency domain, the voltages and currents become phasors of the form $\tilde{V}_k e^{i\omega t}$ and $\tilde{I}_k e^{i\omega t}$, with the admittance matrix given by

$$\mathbf{Y} = \mathbf{G} + i\omega\mathbf{C},\tag{10}$$

where **G** and **C** are the conductance and capacitance matrices, $i = \sqrt{-1}$, and ω is the angular frequency [25]. Both matrices are nearly symmetric for our nonlinear, three-terminal device in figure 2.

The harmonic perturbation option applies a small AC signal with angular frequency ω to each terminal after a DC operating point has been calculated by the semiconductor solver with some voltage applied to the gate, source, and drain contacts. At each DC operating point (linearization point), COMSOL computes the currents and voltages by differentiating the perturbed solution. To compute the admittance matrix above, we compute the ratio of the current and voltage at each terminal, i.e. [19]

$$Y_{ij} = \frac{I_i}{V_j} \Big|_{V_{k \neq j} = 0}.$$
(11)

We compute these currents at each terminal, given voltage source excitations, as a function of frequency ω in the band of 4–8 GHz relevant to superconducting qubit and resonator frequencies, and extracted the conductance matrix as the real, frequency-independent part of **Y** and the capacitance matrix as the derivative of the imaginary part of **Y** from (10)

$$G_{ij} = \operatorname{Re}\{Y_{ij}(\omega)\}\tag{12}$$

$$C_{ij} = \operatorname{Im}\left\{\frac{\mathrm{d}Y_{ij}(\omega)}{\mathrm{d}\omega}\right\},\tag{13}$$

where $dY_{ij}(\omega)/d\omega$ is a constant in our case, as we omit the junction inductance L_{J_0} leading to discontinuities at resonance frequencies proportional to $(C_{ij}L_{J_0})^{-1/2}$ [26].

The capacitance and conductance matrices $C_{c(d)}$ and $G_{c(d)}$ read

$$\mathbf{C}_{c}[\mathbf{f}\mathbf{F}] = \begin{pmatrix} 13.2 & -13.0 & -0.17\\ -13.0 & 13.2 & -0.17\\ -0.17 & -0.17 & 0.34 \end{pmatrix}$$
(14)

$$\mathbf{C}_{d}[\mathbf{f}\mathbf{F}] = \begin{pmatrix} 15.5 & -0.32 & -0.16\\ -0.32 & 15.5 & -0.16\\ -0.06 & -0.06 & 0.33 \end{pmatrix}$$
(15)

$$\mathbf{G}_{c}[\mu \mathbf{S}] = \begin{pmatrix} 23.3 & -23.3 & -6.38\mathrm{E}-4\\ -23.3 & 23.3 & -6.38\mathrm{E}-4\\ -6.38\mathrm{E}-4 & -6.38\mathrm{E}-4 & 1.28\mathrm{E}-3 \end{pmatrix}$$
(16)

$$\mathbf{G}_{d}[\mu S] = \begin{pmatrix} 38.3 & 0.532 & -3.09E-4\\ 0.532 & 38.3 & -3.09E-4\\ 7.74E-3 & 7.74E-3 & 6.49E-4 \end{pmatrix}.$$
 (17)



Figure 3. Electron concentrations (cm⁻³) on a base-10 logarithmic scale with source-drain bias $V_{sd} = 0$ V for the fully conducting $V_g = 0$ V, intermediate $V_g = -0.5$, -1, -2, -4 V, and fully depleted $V_g = -5$ V operating points. The horizontal axis is a 300 nm span centered on the gate electrode and the vertical axis starts at the contact–2DEG interface at ~117 nm, the 2DEG–InAlAs interface is 18 nm below that, and the InAlAs–InP interface is located at 0 nm. We do not solve for *n* in the regions where we applied the electric charge conservation equations, i.e. in the Al_xO_y regions not shown, yet the electric fields respect the boundary conditions set by those regions.

We include the resistance matrices $\mathbf{R}_{c(d)} = \mathbf{G}_{c(d)}^{-1}$, for later use in section 3.2 where we perform coupled two qubit simulations with HFSS and reference the $R_{c(d),12}$ matrix elements in the lumped element representation of the coupler.

$$\mathbf{R}_{c}[\Omega \times 10^{9}] = \begin{pmatrix} 93.3 & 93.3 & 93.3\\ 93.3 & 93.3 & 93.3\\ 93.3 & 93.3 & 94.1 \end{pmatrix}$$
(18)

$$\mathbf{R}_{d}[\Omega] = \begin{pmatrix} 2.61E+4 & -3.65E+2 & 1.23E+4\\ -3.65E+2 & 2.61E+4 & 1.23E+4\\ -3.07E+5 & -3.07E+5 & 1.54E+9 \end{pmatrix}.$$
(19)

Note that the capacitance, conductance, and resistance matrices away from the fully conducting operating point are not symmetric. The departure of the capacitance matrices from symmetry likely stems from numerical imprecision. The conductance discrepancies we attribute to the observed frequency dependence of the real part of **Y**. In the symmetric setting, we expect Re{**Y**(ω)} to be constant with respect to ω , but we find that it varies linearly with ω . This frequency dependence we model as Re{**Y**(ω)} = ω **g** and report **G** = **g** $\Delta \omega$, where **g** has units of Ω^{-1} *s* and $\Delta \omega$ is the frequency step used by the harmonic perturbation study.

The matrix elements of interest, $C_{12} = C_{21}$, $G_{12} = G_{21}$, and $R_{12} = R_{21}$ represent the effective capacitance, conductance, and resistance between the source and drain terminals. These terminals form capacitive contacts with any pair of qubits. The capacitance tuning ratio *r*, or on/off contrast of the 2DEG coupler is given by $r_C = C_{c,12} / C_{d,12} \approx 40$. Similarly, the ratio of conductances is $r_G = G_{c,12} / G_{d,12} \approx 43$. A back of the envelope calculation of the charge concentration in the 2DEG between the source and drain contacts, using expressions for the conductivity $\sigma = \mu_n en$ and conductance $G_{12}/d_0 = \sigma$ gives

$$n_{c,\text{eff}} = \frac{G_{c,12}}{\mu_{\text{n}}ed_0} = 2.0\text{E} + 17\,\text{cm}^{-3}$$
(20)

$$n_{d,\text{eff}} = \frac{G_{d,12}}{\mu_{n}ed_{0}} = 4.6\text{E} + 15\,\text{cm}^{-3}.$$
 (21)

These *effective* charge concentrations agree with figure 3 and link the change in capacitance and conductance with a change in carrier concentration between the source and drain contacts.



3. Results

We report the gate voltage dependence of the capacitance and conductance matrices computed in the previous section at intermediate DC operating points between the fully conducting ($V_g = 0$ V) and fully depleted ($V_g = -5$ V) limits. Figure 4 shows that the source-drain capacitance $C_{12} = C_{21}$ saturates quickly, as $V_g < -1$ V. This is a desired feature for practical tunable couplers, as lower operating voltages are preferred to reduce the active heat load from DC control signals [27]. The conductances $G_{12} = G_{21}$ follow a similar trend, with the other matrix elements following a different V_g -dependence than the capacitance matrix elements.

3.1. Coupler loss estimates

We give bounds on the losses introduced by the 2DEG coupler from experimental measurements of the high participating gate dielectrics and InGaAs upper layer, along with the other layers in the device stack. In table 2, we compute the electric field participation ratios p_j following the procedure developed by [28, 29]. The relaxation time T_1 at a given angular frequency ω , as a function of the dielectric material properties and geometric factors, reads [29]

$$T_1^{-1} = \frac{\omega}{Q} = \omega \sum_j \frac{p_j}{Q_j} + \Gamma_0 \tag{22}$$

$$Q_j^{-1} = \tan \delta_j \tag{23}$$

$$p_j = W_e^{-1} t_{\text{oxide}} \varepsilon_{1,j} \int_{S_j} |\mathbf{E}|^2 \, \mathrm{d}S \tag{24}$$

$$W_{\rm e} = \int_{V} |\mathbf{E}|^2 \mathrm{d}V \tag{25}$$

where W_e is the electric field energy density stored in the volume of the entire geometry *V*, Q_j are the quality factors, tan δ_j are the loss tangents, $\varepsilon_{1,j}$ are the real parts of the dielectric function, and t_{oxide} is the thickness of the participating lossy surface, assumed to be 3 nm for all materials [29]. The participation ratios give the fraction of the electrical energy stored in a given surface S_j relative to the total electrical energy stored in the entire volume of the device. The last term in (22), Γ_0 , includes all other loss mechanisms contributing to T_1 besides dielectric loss [29]. Note, these participation ratios differ from those in subsequent calculations involving *energy* participation ratios referenced to a given mode rather than a particular surface.

Other sources of loss relevant to III–V semiconductor materials, but not considered in this study, include piezoelectricity [30], non-equilibrium quasiparticles [31], cosmic ray muon flux [32], and, to a lesser extent, stray magnetic fields [33].

3.2. Integration with circuit QED

3.2.1. Two qubit coupler

In figure 5 we have a microwave circuit model of two transmon qubits coupled by a lumped impedance $Z_{\text{JJ}_c}(\omega) = 1/(1/R + i\omega C)$, where *R* and *C* take the values of R_{12} and C_{12} in either the fully conducting or fully depleted limits of the 2DEG coupler. In the conducting limit, where some current can flow across the coupler

Table 2. Participation ratios p_j , dielectric loss tangents $\tan \delta_j$, layer thicknesses t_j , and estimated dielectric-loss-limited $T_{1,j}$. All $T_{1,j}$ times are referenced to a qubit frequency of $\omega/2\pi = 5$ GHz and $\tan \delta_j^*$ indicates that in the absence of reliable loss tangent data for the individual InAs, InGaAs, InAlAs, and InP layers, we used the low power loss extracted from measurements of an Al patterned CPW resonator on the full III-V stack modeled in this work and measured at 100 mK as an estimate [36].

Depleted	t_j (nm)	<i>p</i> _{j,norm}	$ an \delta_j^*$	$T_1 (\mu s)$
InGaAs (Top)	10	4.19×10^{-2}	$4.1 imes 10^{-4}$	$1.85 imes 10^{+0}$
InAs	4	1.03×10^{-2}	$4.1 imes 10^{-4}$	$7.53 imes10^{+0}$
InGaAs (Bottom)	4	7.85×10^{-3}	$4.1 imes 10^{-4}$	$9.89 imes10^{+0}$
InAlAs	100	2.92×10^{-2}	$4.1 imes 10^{-4}$	$2.67 imes10^{+0}$
Al ₂ O ₃ [37]	50	$9.04 imes10^{-1}$	5×10^{-3}	6.87×10^{-3}
InP	3.5×10^{-3}	6.97×10^{-3}	$4.1 imes 10^{-4}$	$8.95 imes10^{+0}$
Total	—	1	$7.3 imes 10^{-3}$	$6.81 imes10^{-3}$
Conducting	t_j (nm)	<i>p</i> _{j,norm}	$ an \delta_j^*$	$T_1 (\mu s)$
InGaAs (Top)	10	$1.01 imes 10^{-8}$	$4.1 imes 10^{-4}$	$7.69 \times 10^{+6}$
InAs	4	3.73×10^{-9}	$4.1 imes 10^{-4}$	$2.08 imes10^{+7}$
InGaAs (Bottom)	4	4.03×10^{-9}	$4.1 imes 10^{-4}$	$1.93 imes10^{+7}$
InAlAs	100	1.10×10^{-9}	$4.1 imes 10^{-4}$	$7.06 imes 10^{+7}$
Al ₂ O ₃ [37]	50	$9.9999 imes 10^{-1}$	5×10^{-3}	6.24×10^{-3}
InP	$3.5 imes 10^{-3}$	$1.41 imes 10^{-5}$	$4.1 imes 10^{-4}$	$4.43 imes 10^{+3}$
Total	—	1	$7.3 imes 10^{-3}$	$6.24 imes 10^{-1}$



Figure 5. False color geometry of two transmon qubits with the capacitive coupler in between used in the HFSS simulations. Lumped impedances defined in the gold regions of the insets, represent the linear response of the Josephson junctions and capacitive coupling element in the HFSS model.

and act like a Josephson junction, one might consider adding an inductance to the coupler lumped element model. Taking R_{12} to be the normal resistance of a Josephson junction and computing the junction inductance with the Ambegaokar–Baratoff formula [34], the junction inductances would be very small, on the order of a few aH to tens of fH, resulting in high coupler mode frequencies, far outside of the frequency band of the finite element electromagnetic field solver, Ansys HFSS. For this reason and expected small modifications to qubit-qubit interactions, we omit these inductances in our model and use Ansys HFSS to compute the lowest electromagnetic eigenmodes of the device with the two transmon qubits, indexed by *j*, defined as parallel *LC* lumped elements, $Z_{JJ_q,j} = 1/(1/(i\omega L_{q,j}) + i\omega C_{q,j})$. In the following section, we use these eigenmode solutions to estimate the Hamiltonian matrix elements corresponding to qubit–qubit mode and qubit-coupler mode coupling strengths. We will differentiate between this modal coupling from direct capacitive coupling in the final part of this section, where we calculate the direct charge-charge interaction matrix elements.

Table 3. Eigenmode frequenices and quality factors computed with HFSS in the conducting (c) and depleted (d) limit of the coupler.

Qubit index	$\omega/2\pi~({ m GHz})$	Q
1 (d)	6.0228	1.7×10^{-7}
2 (d)	8.6135	5.1×10^{-9}
1 (c)	6.0228	$4.5 imes 10^{-8}$
2 (c)	8.6135	$1.3 imes 10^{-9}$

3.2.2. EPRs and quantization

To extract the coupling matrix elements between the qubits in our microwave device layout, we employ the EPR method developed by Minev [35]. This approach goes beyond the larger family of black box quantization methods [26, 38], where the Hamiltonian describes a collection of Josephson junction-based qubits interacting with any number of harmonic modes separates into linear and nonlinear terms.

One can relate the modal decomposition of the classical electromagnetic response, e.g. impedance, admittance, or electromagnetic energies, with the linear parts of the Hamiltonian. Additional inputs describing the Josephson junction energy scales, E_J and E_C , related to the inductive and capacitive energies of the junction, account for the nonlinear terms. The total Hamiltonian, accounting for M modes, in the dispersive regime and under the rotating wave approximation, reads

$$H = H_{\rm lin} + H_{\rm nl} \tag{26}$$

$$H_{\rm lin}/\hbar = \sum_{m=1}^{M} \omega_m a_m^{\dagger} a_m \tag{27}$$

$$H_{\rm nl}/\hbar = -\sum_{m=1}^{M} \left(\Delta_m a_m^{\dagger} a_m + \frac{1}{2} \alpha_m a_m^{\dagger 2} a_m^2 \right) + \frac{1}{2} \sum_{m \neq n} \chi_{mn} a_m^{\dagger} a_m a_n^{\dagger} a_n,$$
(28)

where the Lamb shifts Δ_m , cross-Kerr coefficients χ_{mn} , and anharmonicities α_m are given by [35]

$$\Delta_m = \frac{1}{2} \sum_{n=1}^{M} \chi_{mn} \tag{29}$$

$$\chi_{mn} = -\sum_{i \in J} \frac{1}{2} \frac{\hbar \omega_m \omega_n}{4E_{J_i}}$$
(30)

$$\alpha_m = \frac{1}{2}\chi_{mm}.\tag{31}$$

The cross- and self-Kerr (anharmonicities) coefficients extracted with the pyEPR Python package [15, 35] are given by the entries of the $\chi_{c(d)}$ matrix in the conducting (c) and depleted (d) limits of the coupler

$$\frac{1}{2\pi} \boldsymbol{\chi}_{c} \left[\text{MHz} \right] = \begin{pmatrix} 223 & 67.1\\ 67.1 & 223 \end{pmatrix}$$
(32)

$$\frac{1}{2\pi} \boldsymbol{\chi}_d \,[\mathrm{MHz}] = \begin{pmatrix} 129 & 1.02\\ 1.02 & 129 \end{pmatrix}. \tag{33}$$

The rows and columns of $\chi_{c(d)}$ correspond to qubits 1 and 2. Note that the diagonal entries include the 1/2 factor in the definition of the anharmonicities as in (31). The eigenfrequencies and quality factors are recorded in table 3 and follow from the HFSS eigenmode solutions.

3.2.3. Extraction of the exchange interaction

To compute the charge–charge interaction strength between the transmon qubits in our HFSS model, we consider the capacitance matrix associated with a persistent current or flux qubit following the derivation by Orlando [39]. For details on the derivation of the capacitance matrix, see appendix B. The Hamiltonian for the coupled two transmons, written in terms of the Josephson junction phases φ_j and node charges q_j , is given by

$$H = \frac{1}{2}\mathbf{Q}^{T}\mathbf{C}^{-1}\mathbf{Q} + U(\varphi)$$
(34)

$$U(\varphi) = \sum_{j} E_{\mathbf{J}_{j}} \left(1 - \cos \varphi_{j} \right)$$
(35)

$$\mathbf{C} = \begin{pmatrix} C_1 + C_3 & -C_3 \\ -C_3 & C_2 + C_3 \end{pmatrix}.$$
 (36)

In (34), the charge-charge matrix elements are one half the entries of the inverse of the capacitance matrix. We numerically inverted C in (36) using values for C_1 , C_2 obtained from (B.6) and $C_3 = C_{12}(V_g)$ in the depleting and conducting limits. This matrix $\frac{1}{2}e^2\mathbf{C}_{c(d)}^{-1}$ is given by

$$\frac{1}{2}e^{2}\mathbf{C}_{c}^{-1}[\mathrm{MHz}] = \begin{pmatrix} 179 & 21.3\\ 21.3 & 179 \end{pmatrix}$$
(37)

$$\frac{1}{2}e^{2}\mathbf{C}_{d}^{-1}[\mathrm{MHz}] = \begin{pmatrix} 200 & 0.66\\ 0.66 & 200 \end{pmatrix}.$$
(38)

The ratio of the off diagonal elements in (37) and (38) recovers an on/off interaction ratio of more than one order of magnitude, $r_{int} \approx 32$.

We emphasize here that the off-diagonal charge-charge interaction matrix elements give a more accurate description of the coupling between the qubits mediated by the 2DEG coupler than the EPR calculations of the cross-Kerr coefficients.

A more detailed treatment of the coupler including the parasitic capacitances from the Maxwell capacitance matrices in (14) and (15) give three-by-three coupling capacitance matrices in the Lagrangian of the form in (B.11). The modified charge–charge interaction matrix elements in the full parasitic capacitance model is given by

$$\frac{1}{2}e^{2}\mathbf{C}_{c,\text{para}}^{-1}[\text{MHz}] = \begin{pmatrix} 159 & 16.9 & 44.1\\ 16.9 & 108 & 44.1\\ 44.1 & 44.1 & 2.86\text{E}+4 \end{pmatrix}$$
(39)

$$\frac{1}{2}e^{2}\mathbf{C}_{d,\text{para}}^{-1}[\text{MHz}] = \begin{pmatrix} 172 & 0.552 & 43.0\\ 0.552 & 113 & 43.0\\ 43.0 & 43.0 & 2.99\text{E}+4 \end{pmatrix}.$$
(40)

In the parasitic capacitance model, we find an on/off interaction ratio of $r_{int} \approx 31$. Figure 6 illustrates the excellent agreement between the simplified and parasitic capacitances as a function of the gate voltage. Although the simplified model does not account for the parasitic capacitances C_{13} , C_{31} , C_{23} , and C_{32} , it captures the behavior of the charge-charge exchange matrix elements accurately, as the parasitic contributions do not significantly change the values of C_{12}^{-1} .

3.3. Estimation of coupler coherence limit

To estimate the total coherence limit of our coupler in the two qubit device in figure 7, a back-of-theenvelope calculation of the energy stored in the coupler surface, in either qubit mode, gives an electric field participation ratio on the order of 10^{-3} , resulting in a coherence limit of a few to tens of μ s, when considering the loss to be dominated by the gate dielectric and the top InGaAs layer. This is consistent with previous studies of transmon qubits whose participations are near unity in their given mode [15, 26] and gives us further confidence that a coupler of a similar geometry could support fast parametric operations with moderate coherence.







Figure 7. Electric field magnitude (dB scale to enhance color contrast) for the first two eigenmode solutions computed with HFSS. (a) 6.0228 GHz and (b) 8.6135 GHz qubits in the fully depleted limit of the coupler.

4. Discussion

From our COMSOL simulations of the 2DEG semiconductor physics, electrostatic and electric current analyses, we modeled a tunable capacitor with an order of magnitude on/off contrast. The numerical results agree with the schematic picture of modulating a parallel plate geometry by gating a high mobility 2DEG. At the level of estimating the lumped capacitance and resistance inputs to HFSS, our models incorporate 2D semiconductor behavior in greater detail than previous mixed experimental/computational reports [13, 17].

Two models of the coupler, with and without the parasitic capacitances extracted from COMSOL, give similar on/off interaction ratios and absolute interaction strengths. By simulating the full capacitance matrix of the multi-terminal coupler device, we motivating the choice of single gate over multi-gate coupler designs [40]. Both the simplified and parasitic capacitance estimates of the interaction strengths fall between hundreds of kHz to tens of MHz of coupling, on the same order of magnitude as flux-tunable couplers [3, 14].

Our coherence estimates further emphasize that incorporating our coupler design with existing transmon qubit designs comes at a modest reduction in system coherence. With coupler coherences limiting the system coherence to tens of μ s and expected improvements in the base coherences of the coupler materials, we are optimistic that future couplers using a similar operational principle as 2DEG coupler may incur a lower system coherence penalty with the same low participation as modeled here.

This work has implications in the quantum annealing context, where both inductive and capacitive coupling may lead to nonstoquastic Hamiltonians, those that cannot be simulated by quantum Monte Carlo techniques due to the sign problem [41]. By coupling conjugate degrees of freedom, charge and flux, gate-based superconducting qubit systems also stand to benefit from a richer native gate set, e.g. XX, YY, and ZZ [42, 43].

5. Conclusion

We simulated a 2DEG-based, voltage-controlled tunable coupler compatible with superconducting qubits. With an estimated tuning ratio of one order of magnitude and similarly reduced conductance in the off state, our proposed design is an excellent candidate for capacitively coupling superconducting qubits. HFSS simulations and subsequent capacitance matrix inversion analysis suggest that the coupling matrix elements exhibit the same range of tunability, and estimates of the loss suggest that the gate oxides limit the lifetime of the coupler to nearly ten μ s and the top InGaAs layer limits coupler lifetimes to several tens of μ s. Low loss gate dielectrics such as tantalum oxide [44] and hexagonal boron nitride [45], along with improvements in the fabrication of the III–V stack may increase these coherence limits in the near term. Subsequent design iterations may look to reduce the parasitic capacitances with geometric optimization techniques to maximize contrast and minimize stray interactions [46]. A tunable capacitive element may also serve a complementary role with tunable inductive elements to realize nonstoquastic Hamiltonians in quantum annealing systems [41]. Our tuning and coherence estimates, coupled with the benefit of exponential suppression of charge noise over first order sensitivity flux noise in SQUID-based couplers, give us confidence that voltage-controlled coupling elements of the form developed here have the potential to supplant and complement their inductive counterparts in superconducting qubit systems.

Data availability statement

The data that support the findings of this study are openly available at the following URL/DOI: https://doi.org/10.5281/zenodo.8125613.

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Appendix A. III–V ternary alloy parameter calculations

Following the standard linear and quadratic interpolation schemes for III-V ternary alloys $A_x B_{1-x}C$, with composition parameter *x* and in terms of experimentally measured values of their binary constituents, AB and BC, we have the lattice constant *a*, energy gap *E*, and effective mass at the Γ point $m^{\Gamma*}$ as [47]

$$a_{A_xB_{1-x}C} = xa_{AC} + (1-x)a_{BC}, \tag{A.1}$$

$$E_{A_xB_{1-x}C} = xE_{AC} + (1-x)E_{BC} + x(1-x)E_{AB},$$
(A.2)

$$m_{A_{x}B_{1},xC}^{\Gamma*} = xm_{AC}^{\Gamma} + (1-x)m_{BC}^{\Gamma} + x(1-x)m_{AB}^{\Gamma}.$$
(A.3)

Similarly, the hole effective masses follow from a quadratic interpolation scheme of the AB, AC binary components as computed from a spherical band approximation of the valence band edge [47]

$$m_{\rm p,dos} = \left(m_{\rm lh}^{3/2} + m_{\rm hh}^{3/2}\right)^{2/3},$$
 (A.4)

$$m_{\rm p,c} = \frac{m_{\rm lh}^{5/2} + m_{\rm hh}^{5/2}}{m_{\rm p,dos}},\tag{A.5}$$

$$m_{\rm p,c,A_x B_{1-x}C} = xm_{\rm p,c,AC} + (1-x)m_{\rm p,c,BC},$$
 (A.6)

$$m_{\rm p,dos,A_x B_{1-x}C} = x m_{\rm p,dos,AC} + (1-x) m_{\rm p,dos,BC}.$$
 (A.7)

We recognize that the spherical band approximation may not apply to the III–V materials in our study, but it gives an estimate for density of states and conduction band effective masses that are inputs to the COMSOL semiconductor module materials models.

To estimate the conduction band offsets between the $In_xAl_{1-x}As$ and $In_xGa_{1-x}As$ layers, we followed another interpolation scheme that computes the absolute conduction band edges E_c using experimentally measured parameters of InAs, AlAs, and GaAs [48]

$$E_{\rm c} = E_{\rm v,avg} + \frac{\Delta_0}{3} + E_{\rm g} + \Delta E_{\rm c}^{\rm hy},\tag{A.8}$$

$$\Delta E_{\rm c} = E_{\rm c}^{\rm B} - E_{\rm c}^{\rm A},\tag{A.9}$$

where $E_{v,avg}$ is the average valence band edge, Δ_0 is the spin-orbit splitting in the absence of strain, E_g is the band gap energy, and ΔE_c^{hy} is the shift of the conduction band edge due to hydrostatic strain. These parameters are calculated from the following expressions with coefficients C_{ij} read-off from table 3 compiled by Krijn [48]

$$E_{\rm v,avg} = \sum_{i=1}^{2} C_{i0}(E_{\rm v,avg}) x^{i}, \qquad (A.10)$$

$$\Delta_0 = \sum_{i=1}^2 C_{i0}(\Delta_0) x^i, \tag{A.11}$$

$$\Delta E_{\rm c}^{\rm hy} = \frac{\Delta a(x)}{a(x)} \sum_{i=0}^{1} C_{i0} (\Delta E_{\rm c}^{\rm hy}) x^{i}, \tag{A.12}$$

$$\Delta a(\mathbf{x}) = a_0 - a(\mathbf{x}). \tag{A.13}$$

Appendix B. Charge-charge interaction matrix element derivation

Starting from the two transmon circuit coupled by a voltage-controlled Josephson junction (our 2DEG coupler) in figure B1(b), with phases $\varphi_1, \varphi_2, \varphi_3$ referring to the left, right, and coupling junctions, respectively, we have [39]

$$\varphi_1 - \varphi_2 + \varphi_3 = -\frac{2\pi}{\Phi_0} \Phi_{\text{ext}},\tag{B.1}$$

where Φ_{ext} is the flux threading the loop formed by the three Josephson junctions as in a typical flux qubit circuit. For a finite Φ_{ext} , the potential energy U is given by

$$U(\varphi) = \sum_{j} E_{J_{j}}(1 - \cos\varphi_{j})$$

= $E_{J_{1}}(1 - \cos\varphi_{1}) + E_{J_{2}}(1 - \cos\varphi_{2})$
+ $E_{J_{3}}(1 - \cos(\varphi_{2} - \varphi_{1} - 2\pi\Phi_{ext}/\Phi_{0})),$ (B.2)

with the signs on the phases following figure B1(b), preserving the conventions chosen in [39]. We order the phases in a single column vector as

$$\varphi = \begin{pmatrix} \varphi_1 \\ \varphi_2 \end{pmatrix}. \tag{B.3}$$

Setting $\Phi_{\text{ext}} = 0$, we compute the kinetic energy *T* by using the Josephson equation relating the voltages at nodes with $k = \{1, 2\}$, $V_k = (\Phi_0/2\pi)\dot{\varphi}_k$ and the definition of *T* in terms of $\dot{\varphi}_k$

$$T = \frac{1}{2} \left(C_1 V_1^2 + C_2 V_2^2 + C_3 V_3^2 \right)$$

= $\frac{1}{2} \left(\frac{\Phi_0}{2\pi} \right)^2 \left(C_1 \dot{\varphi}_1^2 + C_2 \dot{\varphi}_2^2 + C_3 \left(\dot{\varphi}_2 - \dot{\varphi}_1 \right)^2 \right)$
= $\frac{1}{2} \left(\frac{\Phi_0}{2\pi} \right)^2 \dot{\varphi}^T \mathbf{C} \dot{\varphi},$ (B.4)

and reading off the capacitance matrix

$$\mathbf{C} = \begin{pmatrix} C_1 + C_3 & -C_3 \\ -C_3 & C_2 + C_3 \end{pmatrix}.$$
 (B.5)

Relating the total capacitances (both the intrinsic junction and external capacitance, commonly referred to as C_{Σ} [49]) shunting the junctions, C_1 , C_2 , to the anharmonicities extracted from the EPR calculations, we have, from the asymptotic expressions derived by Koch *et al* [9]

$$C_k = \frac{e^2}{2E_{\rm C}} \simeq -\frac{e^2}{2\alpha_k} \tag{B.6}$$

and we take $C_3 = C_{12}(V_g)$, the gate voltage-dependent capacitance across the 2DEG coupler. The classical Lagrangian \mathcal{L} and Hamiltonian \mathcal{H} associated with the kinetic and potential energies above, then read [39]

$$\mathcal{L}(\boldsymbol{\varphi}, \dot{\boldsymbol{\varphi}}) = T - U$$

= $\frac{1}{2} \left(\frac{\Phi_0}{2\pi}\right)^2 \dot{\boldsymbol{\varphi}}^T \mathbf{C} \dot{\boldsymbol{\varphi}} - \sum_j E_{\mathbf{J}_j} (1 - \cos \varphi_j)$ (B.7)

$$\mathcal{H} = \mathbf{P}^{T} \dot{\boldsymbol{\varphi}} - \mathcal{L}$$

= $\frac{1}{2} \mathbf{Q}^{T} \mathbf{C}^{-1} \mathbf{Q} + U(\boldsymbol{\varphi})$ (B.8)

$$P_{j} = \frac{\partial \mathcal{L}}{\partial \dot{\varphi}_{j}} = \left(\frac{\Phi_{0}}{2\pi}\right)^{2} \sum_{k} C_{jk} \dot{\varphi}_{k}, \ \mathbf{Q} = \frac{2\pi}{\Phi_{0}} \mathbf{P}.$$
(B.9)

We take the form of the quantized Hamiltonian to be the same as the classical one in (B.8) with classical variables promoted to operators, and identify the charge–charge matrix elements as $e^2[\mathbf{C}^{-1}]_{ii}/2$. Similarly, we



write the Lagrangian and identify the capacitance matrix corresponding to the parasitic capacitance model given by the circuit in figure B1(c) as

$$\mathcal{L} = \frac{1}{2} \left(\frac{\Phi_0}{2\pi} \right)^2 \left[(C_1 + C_{11}) \dot{\varphi}_1^2 + (C_2 + C_{22}) \dot{\varphi}_2^2 + C_{33} \dot{\varphi}_3^2 + C_{12} (\dot{\varphi}_2 - \dot{\varphi}_1)^2 + C_{13} (\dot{\varphi}_3 - \dot{\varphi}_1)^2 + C_{23} (\dot{\varphi}_2 - \dot{\varphi}_3)^2 \right] - U(\varphi)$$
(B.10)

$$\mathbf{C} = \begin{pmatrix} \widetilde{C}_{11} & -C_{12} & -C_{13} \\ -C_{12} & \widetilde{C}_{22} & -C_{23} \\ -C_{13} & -C_{23} & \widetilde{C}_{33} \end{pmatrix},$$
(B.11)

where $\widetilde{C}_{11} = C_1 + C_{11} + C_{12} + C_{13}$, $\widetilde{C}_{22} = C_2 + C_{22} + C_{12} + C_{23}$, and $\widetilde{C}_{33} = C_{13} + C_{23} + C_{33}$.

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